

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	66	dynamical\$2 same configur\$4 same microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 12:20
S1	40	description adj4 hardware adj4 resource	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:09
S2	0	description adj4 hardware adj4 resource adj6 extensible adj3 language	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 16:56
S3	18876	xml or (extensible adj markup adj language)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 16:57
S4	37	(xml or (extensible adj markup adj language)) same ic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:03
S5	421	configur\$3 adj3 microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:04
S6	190	configur\$3 adj microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:04
S7	40	S1 and S1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:05
S8	0	access\$3 adj5 description adj4 hardware adj4 resource	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/20 06:49
S9	40	S1 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:06

S10	0	S1 and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:06
S11	4310	description adj4 hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:06
S12	1	S6 and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 09:11
S13	1	S6 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:07
S14	4227	hardware adj2 language	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:09
S15	4	S6 and S14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:11
S16	9	S3 and S5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/19 17:11
S17	32986	acess\$3 sams description adj4 hardware adj4 resource	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/20 06:49
S18	4	access\$3 same (description adj4 hardware adj4 resource)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/20 06:54
S19	180	access\$3 same (description adj4 hardware)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/20 06:54

S20	24	access\$3 same (description adj4 hardware) same circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/20 06:54
S21	40	description adj4 hardware adj4 resource	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:19
S22	6366	user adj2 module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:19
S23	577	(user adj2 module) same configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:20
S24	142	(user adj2 module) same configuration same select\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:20
S25	14	(user adj2 module) same configuration same select\$3 same hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 10:00
S26	2	"20030131334"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:50
S27	2	relationship and S26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:50
S28	1	rlc and S27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:50
S29	5523	microcontroller same configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:09

S30	815	select\$3 same microcontroller same configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:14
S31	10709	hdl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:10
S32	8121	access\$3 adj5 hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:10
S33	5	S31 and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:10
S34	69	select\$3 same microcontroller same configuration same hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:19
S35	45	generat\$3 same configuration same information same microcontroller same select\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 17:20
S36	17	(user adj2 module) same configuration same select\$3 same hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 10:02
S37	51325	microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 10:03
S38	6	S36 and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 10:06
S39	2	"6578174".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 10:06

S40	23487	xml	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 10:06
S41	1	S39 and S40	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 10:07
S42	1	ic and S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/27 12:19